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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	09/898583	Docket Number	CNTR.2020		
Filed	7/3/2001	Group Art Unit	2183		
Examiner	HENRY TSAI Customer No. 23669				
Application Title	APPARATUS AND METHOD FOR DENSELY PACKING A BRANCH INSTRUCTION PREDICTED BY A BRANCH TARGET ADDRESS CACHE AND ASSOCIATED TARGET INSTRUCTIONS INTO A BYTE.WIDE INSTRUCTION BUFFER				
First Named Inventor	G. GLENN HENRY				

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449, submitted under 37 CFR 1.97(d) and 1.97(e)(2), listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seq. Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

The enclosed documents may have markings thereon. No significance is intended to be attached to the markings. Attached hereto is a check for the fee set forth in 37 CFR 1.17(p).

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Respectfully submitted,

E. ALAN DAVIS

Reg. No. 39,954

Tel.: (512) 301-7234

Date 7-9-05

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Complete if Known to tiline for form 1449-PTO **Application Number** 09/898583 INFORMATION DISCLOSURE Filing Date 7/3/2001 STATEMENT BY APPLICANT First Named Inventor G. Glenn Henry (Use as many sheets as necessary) Art Unit 2183 **HENRY TSAI Examiner Name** of Attorney Docket Number CNTR.2020 1 4

Examiner Initials*	Cite No.¹	Document Number Number-Kind Code² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
÷		5706491	01-06-1998	Steven C. McMahan	
		5313634	05-17-1994	James Eickemeyer	
		5961629	10-05-1999	Nguyen et al	
		5832289	11-03-1998	Shaw et al	
		5828901	10-27-1998	O'Toole et al	
		5864707	01-26-1999	Tran et al	
		6233676	05-15-2001	Henry et al	
		5623615	04-22-1997	Salem et al	
		6175897	01-16-2001	Ryan et al	
		5734881	03-31-1998	White et al	
		6260138	07-10-2001	Jeremy G. Harris	
		6823444	11-23-2004	Henry et al	
		6256727	07-03-2001	Robert Greg McDonald	
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		5881260	03-09-1999	Raje et al	
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:		5142634	08-25-1992	Fite et al	
		5163140	11-10-1992	Stiles et al	
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		5687360	11-11-1997	Po-Hua Chang	
		5761723	06-02-1998	Black et al	
		5867701	02-02-1999	Brown et al	
		5964868	10-12-1999	Gochman et al	
		5974543	10-26-1999	Hilgendorf et al	
		5978909	11-02-1999	Oded Lempel	

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Examiner Initials*	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines,
minais	No.¹	Number-Kind Code ² (if known)	MM-DD-YYYY	Applicant of Cited Document	where Relevant Passages or Relevant Figures Appear
		6088793	07-11-2000	Liu et al	
		6151671	11-21-2000	D'Sa et al	
		6314514	11-06-2001	Thomas C. McDonald	
		6502185	12-31-2002	Keller et al	
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		5513330	04-30-1996	David R. Stiles	
		6457120	09-24-2002	Balaram Sinharoy	
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		6308259	10-23-2001	David B. Witt	
		5623614	04-22-1997	Van Dyke et al	
		5768576	06-16-1998	Hoyt et al	

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Examiner Initials*	Cite No.'	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		6170054	01-02-2001	Mitchell Alexander Poplingher	
		6279106	08-21-2001	James S. Roberts	
		2002194464	12-19-2002	Henry et al	
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		20040139292	07-15-2004	Thomas C. McDonald	
		20050076193	04-07-2005	· Henry et al.	
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Substitute for form 1449-PTO				Application Number	09/898583
INFORMATION DISCLOSURE				Filing Date	7/3/2001
S	STATEMENT BY APPLICANT				G. Glenn Henry
	(Use as many sheets as i	Art Unit	2183		
				Examiner Name	HENRY TSAI
Sheet	4	of	4	Attorney Docket Number	CNTR.2020

	NON BATENT LITERATURE DOCUMENTS	
1	NON PATENT LITERATURE DOCUMENTS	
Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
AA	YEH ET AL. Alternative Implementation of Two-Level Adaptive Branch Prediction. 19th Annual International Symposium on Computer Architecture. Pp124-134. May 19-21, 1992. Gold Coast, Australia	, X,
AB	CHANG ET AL. Alternative Implementations of Hybrid Branch Predictors. Proceedings of MICRO-28. 1995. IEEE.	
AC	MC FARLING, SCOTT. WRL Technical Note TN-36. "Combining Branch Predictors. June 1993. Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301	
AD	BRAY ET AL. Strategies For Branch Target Buffers. Technical Report No. CSL-TR-91-480. June 1991.	
AE	SAKAMOTO ET AL. Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor. Pp 208-216. Mittsubishi Electric Corp. 4-1 Mizuhara, Itami, Hyogo 664. Japan, 1996. IEEE.	
	AA AB AC AD	Mo.¹ magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. YEH ET AL. Alternative Implementation of Two-Level Adaptive Branch Prediction. 19th Annual International Symposium on Computer Architecture. Pp124-134. May 19-21, 1992. Gold Coast, Australia CHANG ET AL. Alternative Implementations of Hybrid Branch Predictors. Proceedings of MICRO-28. 1995. IEEE. MC FARLING, SCOTT. WRL Technical Note TN-36. "Combining Branch Predictors. June 1993. Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301 BRAY ET AL. Strategies For Branch Target Buffers. Technical Report No. CSL-TR-91-480. June 1991. AD SAKAMOTO ET AL. Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor. Pp 208-216. Mittsubishi Electric Corp. 4-1 Mizuhara, Itami, Hyogo 664, Japan, 1996. IEEE.

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